

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of:

Ji Yong PARK et al.

Application No. 10/690,507

Group Art Unit: 1792

Confirmation No. 6043

Filed: October 23, 2003

Examiner: Matthew J. Song

For: METHOD FOR MANUFACTURING POLYCRYSTALLINE SILICON THIN FILM AND  
THIN FILM TRANSISTOR FABRICATED USING POLYCRYSTALLINE SILICON THIN  
FILM MANUFACTURED BY THE MANUFACTURING METHOD

**AMENDMENT**

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

**September 8, 2008**

Sir:

This is in response to the Office Action mailed June 12, 2008, and having a period for response set to expire on September 12, 2008.

The following responses and remarks are respectfully submitted. Reconsideration of the claims is respectfully requested.